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<u>L11</u>	l3 and l6	24	<u>L11</u>
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<u>L9</u>	(712/33)[CCLS]	131	<u>L9</u>
<u>L8</u>	(710/310,132,314)[CCLS]	621	<u>L8</u>
<u>L7</u>	(710/305-314)![CCLS]	3771	<u>L7</u>
<u>L6</u>	(712/29-38, 225, 229)[CCLS]	1641	<u>L6</u>
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IEEE JNL	IEEE Journal or Magazine
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Citation & Abstract

[Select All](#) [Deselect All](#)**1. FPGA implemented fast two's complement serial-parallel multiplier with PCI interface**

Khalil, A.H.; Ashour, M.A.; Salama, A.E.; Saleh, H.I.;
[Microelectronics, 1998, ICM '98, Proceedings of the Tenth International Conference on](#)
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**2. A flexible compatible PCI interface for nuclear experiments**

Saleh, H.; Engels, R.; Reinartz, R.; Reinhart, P.; Rongen, F.;
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Saleh, H.; Engels, R.; Reinartz, R.; Reinhart, P.; Rongen, F.;
[Nuclear Science, IEEE Transactions on](#)
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**5. A local area system network RHINET-1: a network for high performance parallel computing**

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- ☐ **7. FPGA based hardware architecture for HIT/DLR hand**
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- ☐ **8. Single-chip FPGA implementation of a cryptographic co-processor**
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- ☐ **23. Dynamic space processor architecture built on commercial open system interface standard**
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